IN THE CLAIMS

Please **amend** Claims 5, 17 and 28 as indicated:

1. (original) A method for accurately determining a device location in an arbitrated loop having a plurality of devices and at least one initiator, wherein each of said plurality of devices having a port bypass circuit, comprising:

(a) enabling said port bypass circuits and initializing said arbitrated loop to determine said initiator's enhanced logical address;

(b) disabling a port bypass circuit associated with a selected device and determining a unique identifier and a physical slot location of said selected device;

(c) saving said unique identifier and said physical slot location of said selected device and enabling said port bypass circuit associated with said selected device;

(d) repeating (b) through (c) for each of said plurality of devices;

(e) disabling said port bypass circuits and performing a loop initialization of said arbitrated loop to determine a unique identifier for each of said plurality of devices; and

(f) mapping said unique identifiers determined in (e) with said unique identifiers associated with physical slot locations in (c) wherein said physical slot location of each of said plurality of devices is accurately determined.

2. (original) The method as recited in Claim 1, further comprising determining a set of valid physical addresses for said plurality of devices.

3. (original) The method as recited in Claim 2, further including assigning a preferred enhanced logical address to said initiator that does not correspond to an address in said set of valid physical addresses.

4. (original) The method as recited in Claim 1, wherein said disabling a port bypass circuit associated with a selected device includes determining an enhanced logical address of said selected device.

5. (currently amended) The method as recited in Claim 4, further comprising:

comparing said enhanced logical address of said selected device with said physical slot location of said selected device; and

reporting a fault condition in response to said enhanced logical address and said physical slot location of said selected device not being identical due to a connector defect at said physical slot location.

6. (original) The method as recited in Claim 4, further comprising:

comparing said enhanced logical address of said selected device with said physical slot location of said selected device;

responsive to said enhanced logical address and said physical slot location of said selected device not being identical, comparing said physical slot location of said selected device with said enhanced logical addresses of the remainder of said plurality of devices; and

reporting a fault condition in response to said physical slot location of said selected device not equal to one of said enhanced logical address of the remainder of said plurality of devices.

7. (original) The method as recited in Claim 1, wherein said disabling said port bypass circuits and performing a loop initialization includes determining an enhanced logical address for each of said plurality of devices.

8. (original) The method as recited in Claim 1, wherein said arbitrated loop is a fibre channel arbitrated loop (FC-AL).

9. (original) The method as recited in Claim 1, wherein said unique identifier is a world wide unique address (WWID) of said device.

10. (original) The method as recited in Claim 1, wherein said saving said unique identifier and said physical slot location of said selected device includes utilizing a first table.

11. (original) The method as recited in Claim 10, wherein said disabling said port bypass circuits and performing a loop initialization includes constructing a second table to store said unique identifiers.

12. (original) The method as recited in Claim 11, wherein said mapping said unique identifiers includes matching a unique identifier in said second table with an identical unique identifier in said first table to obtain a physical slot location.

13. (original) A computer program product, comprising:

a computer-readable medium having stored thereon computer executable instructions for implementing a method for accurately determining a device location in an arbitrated loop having a plurality of devices and at least one initiator, wherein each of said plurality of devices having a port bypass circuit, said computer executable instructions when executed perform the steps of:

(a) enabling said port bypass circuits and initializing said arbitrated loop to determine said initiator's enhanced logical address;

(b) disabling a port bypass circuit associated with a selected device and determining a unique identifier and a physical slot location of said selected device;

(c) saving said unique identifier and said physical slot location of said selected device and enabling said port bypass circuit associated with said selected device;

(d) repeating (b) through (c) for each of said plurality of devices;

(e) disabling said port bypass circuits and performing a loop initialization of said arbitrated loop to determine a unique identifier for each of said plurality of devices; and

(f) mapping said unique identifiers determined in (e) with said unique identifiers associated with physical slot locations in (c) wherein said physical slot location of each of said plurality of devices is accurately determined.

14. (original) The computer program product as recited in Claim 13, further comprising determining a set of valid physical addresses for said plurality of devices.

15. (original) The computer program product as recited in Claim 14, further including assigning a preferred enhanced logical address to said initiator that does not correspond to an address in said set of valid physical addresses.

16. (original) The computer program product as recited in Claim 13, wherein said disabling a port bypass circuit associated with a selected device includes determining an enhanced logical address of said selected device.

17. (currently amended) The computer program product as recited in Claim 16, further comprising:

comparing said enhanced logical address of said selected device with said physical slot location of said selected device; and

reporting a fault condition in response to said enhanced logical address and said physical slot location of said selected device not being identical due to a connector defect at said physical slot location.

18. (original) The computer program product as recited in Claim 16, further comprising:

comparing said enhanced logical address of said selected device with said physical slot location of said selected device;

responsive to said enhanced logical address and said physical slot location of said selected device not being identical, comparing said physical slot location of said selected device with said enhanced logical addresses of the remainder of said plurality of devices; and

reporting a fault condition in response to said physical slot location of said selected device not equal to one of said enhanced logical address of the remainder of said plurality of devices.

19. (original) The computer program product as recited in Claim 13, wherein said arbitrated loop is a fibre channel arbitrated loop (FC-AL).

20. (original) The computer program product as recited in Claim 13, wherein said unique identifier is a world wide unique address (WWID) of said device.

21. (original) The computer program product as recited in Claim 13, wherein said saving said unique identifier and said physical slot location of said selected device includes utilizing a first table.

22. (original) The computer program product as recited in Claim 21, wherein said disabling said port bypass circuits and performing a loop initialization includes constructing a second table to store said unique identifiers.

23. (original) The computer program product as recited in Claim 22, wherein said mapping said unique identifiers includes matching a unique identifier in said second table with an identical unique identifier in said first table to obtain a physical slot location.

24. (original) An arbitrated loop network system, comprising:

a plurality of devices, including at least one initiator device, coupled to said arbitrated loop;

a hub, coupled to said plurality of device, having a plurality of port bypass circuits, each of said plurality of port bypass circuits associated with one of said plurality of devices;

(a) means for enabling said port bypass circuits and initializing said arbitrated loop to determine said initiator's enhanced logical address;

(b) means for disabling a port bypass circuit associated with a selected device and determining a unique identifier and a physical slot location of said selected device;

(c) means for saving said unique identifier and said physical slot location of said selected device and enabling said port bypass circuit associated with said selected device;

(d) means for repeating (b) through (c) for each of said plurality of devices;

(e) means for disabling said port bypass circuits and performing a loop initialization of said arbitrated loop to determine a unique identifier for each of said plurality of devices; and

(f) means for mapping said unique identifiers determined in (e) with said unique identifiers associated with physical slot locations in (c) wherein said physical slot location of each of said plurality of devices is accurately determined.

25. (original) The system as recited in Claim 24, further comprising means for determining a set of valid physical addresses for said plurality of devices.

26. (original) The system as recited in Claim 25, further including means for assigning a preferred enhanced logical address to said initiator that does not correspond to an address in said set of valid physical addresses.

27. (original) The system as recited in Claim 24, wherein said disabling a port bypass circuit associated with a selected device includes determining an enhanced logical address of said selected device.

28. (currently amended) The system as recited in Claim 27, further comprising:

comparing said enhanced logical address of said selected device with said physical slot location of said selected device; and

reporting a fault condition in response to said enhanced logical address and said physical slot location of said selected device not being identical due to a connector defect at said physical slot location.

29. (original) The system as recited in Claim 27, further comprising:

comparing said enhanced logical address of said selected device with said physical slot location of said selected device;

responsive to said enhanced logical address and said physical slot location of said selected device not being identical, comparing said physical slot location of said selected device with said enhanced logical addresses of the remainder of said plurality of devices; and

reporting a fault condition in response to said physical slot location of said selected device not equal to one of said enhanced logical address of the remainder of said plurality of devices.

30. (original) The system as recited in Claim 24, wherein said arbitrated loop is a fibre channel arbitrated loop (FC-AL).

31. (original) The system as recited in Claim 24, wherein said unique identifier is a world wide unique address (WWID) of said device.

32. (original) The system as recited in Claim 24, wherein said means for saving said unique identifier and said physical slot location of said selected device includes means for utilizing a first table.

33. (original) The system as recited in Claim 32, wherein said means for disabling said port bypass circuits and performing a loop initialization includes means for constructing a second table to store said unique identifiers.

34. (original) The system as recited in Claim 33, wherein said means for mapping said unique identifiers includes means for matching a unique identifier in said second table with an identical unique identifier in said first table to obtain a physical slot location.